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March 23, 2005

MAIL STOP ISSUE FEE
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: Applicant(s): Kevin E. Sallese
Assignee: Lattice Semiconductor Corporation
Title: Programmable Logic Device With A Memory-Based
Finite State Machine
Serial No.: 10/624,965
Examiner: Chang, Daniel D.
Docket No.: M-15170 US
Filed: 07/21/2003
Group Art Unit: 2183
Confirmation No.: 6289

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) Return Receipt Postcard;
- (2) This Transmittal Letter (in duplicate);
- (3) Amendment under 37 C.F.R. §1.312 (9 pages);
- (4) Issue Fee Transmittal (in duplicate); and
- (5) Submission of Replacement Drawings with two (2) sheets of replacement drawings including Figures 2-4.
- (6) Fee Address Indication Form

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date stated below.

First Class Mail

Tina Kavanaugh
Tina Kavanaugh

March 23, 2005

Respectfully submitted,

Jon W. Hallman
Attorney for Applicants
Reg. No. 42,622



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kevin E. Sallese
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Irvine, California
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SUBMISSION OF REPLACEMENT DRAWINGS

Dear Sir:

Applicants submit two (2) sheets of replacement drawings, consisting of Figures 2-4, in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (949) 752-7040.

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450, on March 23, 2005.


Tina Kavanaugh

March 23, 2005

Respectfully submitted,



Jon W. Hallman
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